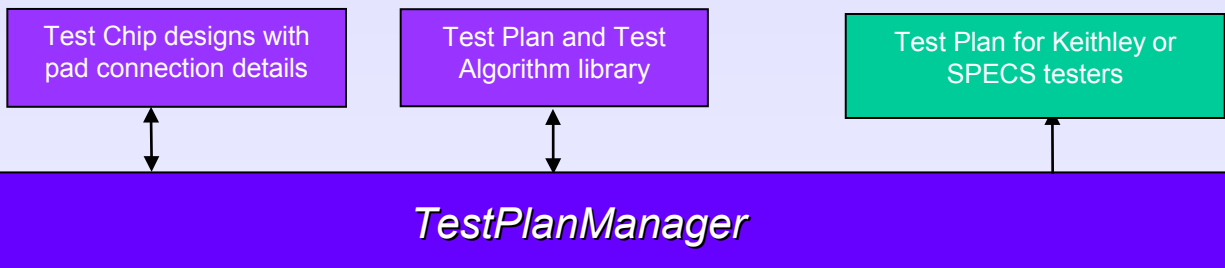


Stone Pillar TestPlanManager

Complete Test Plan and Test Flow Management System

Stone Pillar TestPlanManager offers a complete, integrated, graphically driven system for connecting test chips with test algorithm libraries to create complete test flows and test plans. TestPlanManager presents the user with details of available tests, test chip attributes, and test flow libraries. TestPlanManager makes test plan and test flow creation a simple drag and drop operation. TestPlanManager reduces errors by forcing compliance between device types and test types and makes it simple to apply tiers of tests across all or selected elements within a complete test chip. TestPlanManager reduces errors by easing re-use and providing error checks and reviews.

TestPlanManager works within Stone Pillar Suite which provides easy access to test chip details, supported devices, device targets and limits, and other related information.



The screenshot displays the Silicon Insight Desktop environment with three overlapping windows:

- TestChipBuilder:** Shows a list of modules and their device names.

Module	Device Name
Bipolar_Trenchisolated	1 PMOS,L=0.5,W=2,NumGates=
CAPACITOR_ABAB-COMB	2 PMOS,L=1,W=2,NumGates=2
CAPACITOR_ABAB-HALF-COMB	3 PMOS,L=2,W=2,NumGates=2
CAPACITOR_ABBA-COMB	4 PMOS,L=0.5,W=3,NumGates=
MOSFET_NMOS	5 PMOS,L=1,W=3,NumGates=2
MOSFET_PMOS	6 PMOS,L=2,W=3,NumGates=2
RESISTOR_CONTACT_CHAIN	7 PMOS,L=0.5,W=4,NumGates=
- New Procedure:** Shows configuration for a procedure named 'Beta_proc'.

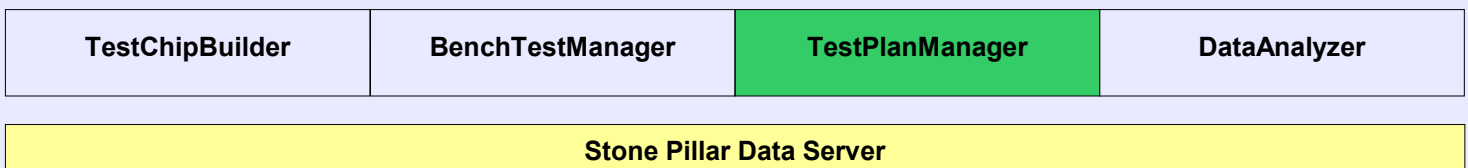
Name	Value
Vmax	4
Ibase	1
- Test Matrix:** Displays a table of test results for various modules.

Module	Length (um)	NumBaseEmitters	NumIslands	TrenchSize (um)	Width (um)	Beta_proc	Beta_Reverse_proc	BipolarBreakdown_proc
1	O1	2	2	0.25	0.4	✓	✓	✓
2	O1	2	3	0.25	0.4	✓	✓	✓
3	O1	2	2	0.25	0.6	✓	✓	✓
4	O1	2	3	0.25	0.6	✓	✓	✓
5	O1	3	2	0.25	0.4	✓	✓	✓
6	O1	3	3	0.25	0.4	✓	✓	✓
7	O2	3	2	0.25	0.6	✓	✓	✓
8	O2	3	3	0.25	0.6	✓	✓	✓
9	O2	4	2	0.25	0.4	✓	✓	✓
10	O2	4	3	0.25	0.4	✓	✓	✓
11	O2	4	2	0.25	0.6	✓	✓	✓
12	O2	4	3	0.25	0.6	✓	✓	✓

Applications

Features

- Easily create complete test plans through simple drag and drop operation and easy GUI-based actions
 - Apply tests or groups of tests to all devices or selected subsets of devices with point and click selection
 - Parameterize tests for maximum re-usability
 - Automatically order tests for maximum efficiency or for considerations such as device failure
 - Automatically export complete test plans for Agilent or Keithley test equipment
 - Automatically import test chip details and test algorithm libraries
- Speeds test plan creation
 - Reduces errors in test plan and test flow creation
 - Automates procedure re-use
 - Provides test library management function
 - Links devices to layout and target information
 - Connects device measurements with experimental conditions, device geometry variations, or design rule experimentation
 - Integrated with complete Stone Pillar Suite for technology development



About Stone Pillar Technologies

Stone Pillar Technologies provides enterprise software solutions for Process & Device Technology Development and Characterization.

Stone Pillar Suite is a complete, broad-span, information management system focused on solving problems in semiconductor process & device development.

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